

Electronic Acknowledgement Receipt

EFS ID:	5164962
Application Number:	09839023
International Application Number:	
Confirmation Number:	2391
Title of Invention:	Optically interconnecting multiple processors
First Named Inventor/Applicant Name:	Kannan Raj
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Attorney Docket Number:	ITL0462US (P9816)
Receipt Date:	16-APR-2009
Filing Date:	20-APR-2001
Time Stamp:	13:11:01
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (5B/08)	ITL0462US_IDS_041609.pdf	575766 <small>a891a87405c779c23a00382bc147055d34888f52da</small>	no	4

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2	Foreign Reference	D4_WO0029888A1.pdf	1134311 2cd967d8ea451ef5f51cd9b3588a88e4e3bc7 1aaf	no	24
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3	NPL Documents	EP_OA_032409.pdf	188751 f6cd42a473bd9485a3b3cd8013449c29878 003bb	no	4
Warnings:					
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4	NPL Documents	D5_Toshiyoshi_Design_And_A nalysis.pdf	660294 754b6a3015bb75da66c527d13651a478ba7 98a1	no	8
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5	NPL Documents	D6_Grant_Integrated_Optical. pdf	525253 99188bd907a00abcf6722b4a3d12759df ca35	no	10
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6	NPL Documents	D7_Livingston_Planar_Diffracti on.pdf	539959 5b406b7d0266e445449912f5de4d67e1b3 98db0	no	8
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